EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	654	(712/227).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/12/21 11:34
L2	179	(712/16).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/12/21 11:35
L3	314	(712/22).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/12/21 11:35
L4	23	Huffman near4 decod$3 near4 align$5	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/12/21 11:37
L5	3	(("5254991") or ("5208593") or ("5686915")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/12/21 11:37
S1	153	(712/16).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:32
S2	229	(712/22).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:32
S3	1	SIMD near4 (data adj1 buffer) near4 (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:38
S4	1	(SIMD near4 (data adj1 buffer)) with (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:34
S5	1	(SIMD near4 (data adj1 buffer)) same (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:34

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S6	1	(SIMD with (data adj1 buffer)) with (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:35
S7	1	(SIMD with (data adj1 buffer)) same (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:35
S8	1	(SIMD same (data adj1 buffer)) same (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:35
S9	1	(SIMD near4 buffer) near4 (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:35
S10	1	(SIMD near4 buffer) with (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:35
S11	1	(SIMD near4 buffer) same (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:36
S12	1	(SIMD near4 buffer) near4 (bit near4 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:36
S13	1	(SIMD near4 buffer) with (bit near4 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:36
S14	1	(SIMD near4 buffer) same (bit near4 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:36
S15	1	(SIMD with buffer) same (bit near4 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:36
S16	1	(SIMD same buffer) same (bit near4 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:36
S17	2	SIMD near4 (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:37

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S18	2	SIMD with (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:37
S19	3	SIMD same (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/09/22 09:37
S20	1	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) near4 (data adj1 buffer) near4 (bit adj1 extension)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/19 13:29
S21	1	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) near4 (data adj1 buffer) near4 (control adj1 unit$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/19 14:04
S22	1	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) with ((data adj1 buffer) near4 (control adj1 unit$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/19 14:05
S23	1	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) same ((data adj1 buffer) near4 (control adj1 unit$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/19 14:05
S24	10	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) same ((data adj1 buffer) near4 control$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/19 17:07
S25	2	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) with ((data adj1 buffer) near4 control$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/19 17:07
S26	0	(double adj1 buffer$3) near4 SIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/21 11:32
S27	17	(double adj1 buffer$3) with SIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/21 11:33
S28	23	(double adj1 buffer$3) same SIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/21 11:34
S29	144	(double adj1 buffer$3) and SIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/21 11:39

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S30	0	((double adj1 buffer$3) near4 concurren$3) and SIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:30
S31	4	((double adj1 buffer$3) near4 port$1) and SIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/21 14:25
S32	32	((double adj1 buffer$3) with port$1) and SIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/21 15:04
S33	48	((double adj1 buffer$3) same port$1) and SIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/21 16:08
S34	58	(double adj1 buffer$3) near4 (concurrent$3 or simultaneous$2 or same?time)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/21 16:08
S35	30	SIMD and "Wilkinson".in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:08
S36	1	SIMD near4 ((data adj1 buffer) near4 (simultaneous$2 or concurrent$2 or same?time))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:08
S37	3	((double adj1 buffer$3) near4 (concurren$3 or simultaneous$3 or same?time)) and SIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:34
S38	0	SIMD near4 (circular adj1 buffer$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:38
S39	0	SIMD near4 (shared adj1 buffer$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:46
S40	0	SIMD with (shar$3 adj1 buffer$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:50
S41	0	SIMD with (shar$3 near4 buffer$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:50

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S42	0	SIMD same (shar$3 near4 buffer$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:50
S43	80	SIMD and (shar$3 near4 buffer$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:50
S44	0	SIMD and ((shar$3 near4 buffer$3) near4 (simultaneous$2 or concurrent$2))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:52
S45	4	SIMD and ((shar$3 near4 buffer$3) with (simultaneous$2 or concurrent$2))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:53
S46	4	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) and ((shar$3 near4 buffer$3) with (simultaneous$2 or concurrent$2))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 15:53
S47	7	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) and ((shar$3 near4 buffer$3) same (simultaneous$2 or concurrent$2))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 16:03
S48	0	(parallel adj1 process$3) near4 ((shar$3 near4 buffer$3) near4 (simultaneous$2 or concurrent$2))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 16:04
S49	1	(parallel adj1 process$3) near4 (shar$3 near4 buffer$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 16:04
S50	5	(parallel adj1 process$3) with (shar$3 near4 buffer$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 16:05
S51	21	(parallel adj1 process$3) same (shar$3 near4 buffer$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/01 16:05
S52	160	(712/16).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 14:59

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S53	236	(712/22).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 14:59
S54	0	(double adj1 buffer$3) near4 SIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 14:59
S55	18	(double adj1 buffer$3) with SIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 15:00
S56	4	SIMD and ((shar$3 near4 buffer$3) with (simultaneous$2 or concurrent$2))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:10
S57	8	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) and ((shar$3 near4 buffer$3) same (simultaneous$2 or concurrent$2))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:12
S58	1	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) near4 (data adj1 buffer) near4 ((data adj1 transfer) or (data adj1 management) or DMA or (bus$2 near4 manaagement))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:14
S59	2	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) with (data adj1 buffer) with ((data adj1 transfer) or (data adj1 management) or DMA or (bus$2 near4 manaagement))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:14
S60	6	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) same (data adj1 buffer) same ((data adj1 transfer) or (data adj1 management) or DMA or (bus$2 near4 manaagement))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:15
S61	2	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) near4 (data adj1 buffer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:15
S62	10	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) near4 (data near4 buffer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:16

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S63	3	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) near4 (input near4 buffer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:25
S64	10	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) with (input near4 buffer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:26
S65	19	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) same (input near4 buffer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:27
S66	34	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) with (data adj1 input)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:31
S67	47	(SIMD or (single adj1 instruction adj1 multiple adj1 data)) with (data near4 buffer$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 16:31
S68	5	port near4 (concurrent near4 (input and output))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 17:51
S69	1	("20020184471").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 19:23
S70	503	(712/227).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/12/10 19:23
S71	576	(712/227).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/01 16:16
S72	164	(712/16).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/09/01 16:16
S73	261	(712/22).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/09/01 16:16

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S74	326	((double near4 buffer$3) or (read$3 near4 writ$3 near4 buffer$3)) and (discrete near4 inverse near4 cosine)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/01 16:26
S75	190	((double near4 buffer$3)) and (discrete near4 inverse near4 cosine)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/01 16:26
S76	169	((double adj1 buffer$3)) and (discrete near4 inverse near4 cosine)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/01 16:26
S77	38	(double adj1 buffer$3) and (discrete near4 inverse near4 cosine) and (bit near4 extension$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/01 16:27
S78	38	(double adj1 buffer$3) and (discrete near4 inverse near4 cosine) and (sign near4 extension$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/01 16:27
S79	38	(double adj1 buffer$3) and (discrete near4 inverse near4 cosine) and (sign near4 extension$1) and (data near4 align$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/01 16:31
S80	38	(double adj1 buffer$3) and (discrete near4 inverse near4 cosine) and (bit near4 extension$1) and (data near4 align$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/01 16:31
S81	1	("6836860").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/06 09:37
S82	610	(712/227).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/18 15:47
S83	173	(712/16).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/04/18 15:47
S84	285	(712/22).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/04/18 15:47

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R Benes, SM Nowick, A Wolfe - Advanced Research in Asynchronous Circuits and Systems, 1998 ... - ieeexplore.ieee.org
A Fast Asynchronous **Huffman Decoder** for ... strlrction ROM, then arc drcornpressrd on dcmtind dirring instructtun cache refill. The **Huffman decoder** is used (IS (1 ...
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High speed pattern matching for a fast **Huffman decoder** - group of 3 »
SB Choi, MH Lee - Consumer Electronics, IEEE Transactions on, 1995 - ieeexplore.ieee.org
Page 1 Choi and Lee: High Speed Pattern Matching for a Fast **Huffman Decoder** 97 Manuscript received November 14, 1994 0098 3063/95 $04.00 © 1995 IEEE HIGH ...
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Huffman decoder architecture for high speed operation and reduced memory - group of 2 »
J Allen, M Boliek, EL Schwartz, D Bednash - US Patent 5,325,092, 1994 - Google Patents
... Allen et al. [54] **HUFFMAN DECODER** ARCHITECTURE FOR HIGH SPEED OPERATION AND REDUCED
MEMORY ... ABSTRACT Various **Huffman decoder** architectures are presented. ...
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DESIGN AND HARDWARE IMPLEMENTATION OF A MEMORY EFFICIENT **HUFFMAN** DECODING - group of 3 »
R Hashemian - IEEE Transactions on Consumer Electronics, 1994 - ieeexplore.ieee.org
... Northern Illinois University Department of Electrical Engineering Abstract- Hardware design of a high speed and memory efficient **Huffman decoder**, introduced in ...
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Method and apparatus for digital **Huffman** decoding - group of 4 »
CS Weaver - US Patent 4,535,320, 1985 - Google Patents
... [54] [75] [73] [21] [22] [51] [52] [58] [56] METHOD AND APPARATUS FOR DIGITAL **HUFFMAN DECODING** Inventor: Charles S. Weaver, Palo Alto, Calif. ...
Cited by 21 - Related Articles - Web Search

A Parallel **Decoder** of Programmable **Huffman** Codes - group of 3 »
BWY Wei, H Meng - IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS FOR VIDEO ..., 1995 - ieeexplore.ieee.org
... technologies. It has been difficult to implement parallel **Huffman decoding**. The ... Fig. I. Parallel **Huffman decoder**. ifi. **DECODING** ...
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Non-destructive lossless image coder - group of 3 »
JH Arbeiter - US Patent 5,060,242, 1991 - Google Patents
... Avariable length code word of from 3 to 15 bits is applied to a **Huffman decoder** 216, such as a ROM look up table, of a **Huffman** and run length **decoding** circuit ...
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Digital image coding using random scanning - group of 2 »
T Savatier, A Delpuch... - US Patent 5,136,371, 1992 - Google Patents
... buffer in the coder. The **decoder** includes a buffer, **Huffman decoder**, threshold adding and inverse DCT. Another such coding/**decoding** ...
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On the joint source-channel decoding of variable-length encodedsources: the BSC case - group of 6 »
KP Subbalakshmi, J Vaisey - Communications, IEEE Transactions on, 2001 - ieeexplore.ieee.org
... These curves correspond to the MAP **decoder** and **Huffman decoder** with and without FEC and are labeled ac- cordingly. From the MSNR ...
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[BOOK] Implementation of a fast MPEG-2 compliant **Huffman decoder** - group of 2 »
MK Rudberg, L Wanhammar - es.isy.liu.se
Page 1. IMPLEMENTATION OF A FAST MPEG-2 COMPLIANT **HUFFMAN DECODER** ... ABSTRACT In this paper a 100 Mbit/s **Huffman decoder** implementation is presented. ...
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A Fast Asynchronous **Huffman Decoder** for ... strlrction ROM, then arc drcornpressrd on dcmtind dirring instructtun cache refill. The **Huffman decoder** is used (IS (1 ...
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Method and apparatus for decoding **Huffman** codes - group of 2 »
P Ruetz, P Tong - US Patent 5,254,991, 1993 - Google Patents
... code is recognized), or in parallel (ie **decode** the entire code ... The latter method of decod- 25 ing **Huffman** codes is ... in the prior art for fast **decoding** Huff -man ...
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A Parallel **Decoder** of Programmable **Huffman** Codes - group of 3 »
BWY Wei, H Meng - IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS FOR VIDEO ..., 1995 - ieeexplore.ieee.org
... from the Symbol Memory is an 8-b **Huffman** decoded (RUN ... to **align** next 32 bits for the subsequent **decoding** operation. ... Our **decoder** uses standard circuit components. ...
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Method and apparatus for performing a parallel speculative **Huffman** decoding using both partial and ... - group of 3 »
K Twardowski - US Patent 6,043,765, 2000 - Google Patents
... then shifting the input data to **align** the next ... of cycles or to increase the **decoding** throughput or ... 2, the decompressor includes **Huffman decoder** 201, which may ...
Cited by 7 - Related Articles - Web Search

System and method for fast **huffman** decoding - group of 2 »
M Keith - US Patent 5,615,020, 1997 - Google Patents
... **decoding** method 10 is repre -sented by **Huffman** symbol **alignment** ... diagram 500 are the
most difficult to **decode** within fast statistical **decoding** method 10. ...
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Method and apparatus for decoding **huffman** codes by detecting a special class - group of 2 »
P Tong, P Ruetz - US Patent 5,181,031, 1993 - Google Patents
... In general, these circuits either **decode** a ... The latter method of **decoding Huffman** codes is known as "fast ... method in the prior art for fast **decoding** Huff -man ...
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Method and structure for decoding **Huffman** codes using leading ones detection - group of 2 »
P Tong, PA Ruetz - US Patent 5,208,593, 1993 - Google Patents
... ing **Huffman** codes is known as "fast **decoding** ... One method in the prior art for fast **decoding** Huff- 50 memory ... memory locations necessary to **decode** a given number ...
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DX JPEG **Huffman decoder** - group of 2 »
RJ D'Ortenzio - US Patent 5,825,312, 1998 - Google Patents
... For the typical **Huffman** code example shown in FIG ... To decompress this data, the

decoder
must ultimately be able ... bits to **align** the register for **decoding** the next ...
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A CONCURRENT MEMORY-EFFICIENT VLC **DECODER** FOR MPEG APPLICATIONS - group of 5 »
CT Hsieh, SP Kim - ieeexplore.ieee.org
... to extract the code and **align** the following ... s detector to implement the high efficient **Huffman decoder**. ... PATTERN MATCHING AND CONCURRENT **DECODING** ALGORITHM Two ...
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Interleaved **Huffman** encoding and decoding method - group of 2 »
FM Nelson, TD Truong, V Kadakia - US Patent 5,686,915, 1997 - Google Patents
... generator32 where it is added onto the **Huffman** portion. ... circuit, which can be used to separately **decode** odd and ... words, can also be used for **decoding** color data ...
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